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APPLICANTS Junichi Yano, Osaka, JAPAN; Hisato Yoshida, Nara, JAPAN; Kimihiko Aiba, Osaka, JAPAN; Katsuyuki Imamura, Osaka, JAPAN; Junichi Mori, Osaka, JAPAN; Junya Yamamoto, Kyoto, JAPAN;					
** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2000-371299 12/06/2000					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 01/29/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 1
ADDRESS 20277					
TITLE INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS					
FILING FEE RECEIVED 1672	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		